



CS20N50

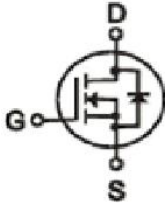
硅 N 沟道功率 MOSFET

Description

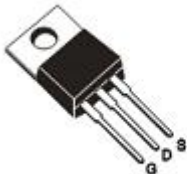

CS20N50 是 N 沟道功率 MOSFET。具有开关速度快，热阻低，输入阻抗高等特点，通常应用于电子镇流器、电子变压器、开关电源等器件。

1、最大额定值 除非另有规定， $T_c = 25^\circ\text{C}$

参数	符号	额定值	单位
漏源电压	V_{DS}	500	V
栅源电压	V_{GS}	± 30	V
漏极电流 ($T_c=25^\circ\text{C}$)	I_D	20	A
漏极电流 ($T_c=100^\circ\text{C}$)	I_D	12.5	A
最大脉冲电流	I_{DM}	80	A
耗散功率	P_{tot}	T0-220C: 260	W
		T0-220F: 45	
最高结温	T_j	150	$^\circ\text{C}$
存储温度	T_{stg}	$-55 \sim 150$	$^\circ\text{C}$
单脉冲雪崩能量	E_{AS}	1200	mJ



$V_{DS}=500\text{V}$
 $R_{DS(ON)}=0.3\Omega$
 $I_D=20\text{A}$

注：漏极电流由最高结温限制



2.电参数 除非另有规定, $T_c = 25^\circ\text{C}$

参数	符号	测试条件	最小值	典型值	最大值	单位
漏源击穿电压	BV_{DSS}	$V_{GS} = 0V, I_D = 250 \mu A$	500			V
击穿电压温度系数	$\frac{\Delta BV_{DSS}}{\Delta T_j}$	$I_D = 250 \mu A,$ Referenced to 25°C		0.6		$V/^\circ\text{C}$
栅极开启电压	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0		4.0	V
漏源漏电流	I_{DSS}	$V_{DS} = 500V, V_{GS} = 0V, T_j = 25^\circ\text{C}$			5	μA
		$V_{DS} = 400V, V_{GS} = 0V, T_j = 125^\circ\text{C}$			100	μA
跨导	g_{fs}	$V_{DS} = 15V, I_D = 10A$ ③		18		S
栅极漏电流	I_{GSS}	$V_{GS} = \pm 30V$			± 100	nA
漏源导通电阻	$R_{DS(on)}$	$V_{GS} = 10V$ $I_D = 10A$ ③		0.24	0.3	Ω
输入电容	C_{iss}	$V_{GS} = 0V,$		2919		pF

潍坊市汇川电子有限公司

地址: 安丘市经济开发区

电话: 0536-4930586

传真: 0536-4930589



输出电容	Coss	V _{DS} =25V, f=		277		
重复传输电容	Crss	1MHz		16		
启动延迟时间	T _{d(on)}	V _{DD} =250V I _D =20A R _G =10 Ω ③		34		nS
上升时间	T _r			65		
关断延迟	T _{d(off)}			82		
下降时间	T _f			45		
栅极电荷	Q _g	V _{DS} = 400V		52		nC
栅源电荷	Q _{gs}	V _{GS} = 10V		12.6		
栅漏电荷	Q _{gd}	I _D = 20A③		18.6		
连续漏源电流	I _{SD}				20	A
脉冲漏源电流	I _{SM}				80	A
二极管正向压降	V _{SD}	T _j =25°C, I _s =2 0A, V _{GS} =0V③			1.5	V
反向回复时间	trr	T _j =25°C, I _s =2 0A,		535		nS
反向回复电荷	Q _{rr}	di/dt=100A/ μs③		5671		uC



3.热特性

参数	符号	最大值		单位
		TO-220C	TO-220F	°C/W
结-壳热阻	Rthjc	0.48	2.78	°C/W
结-环境热阻	RthjA	62.5	62.5	°C/W

注释(Notes):

①脉冲宽度: 以最高结温为限制

②初始结温=25°C, VDD =50V, L=10mH, RG =25Ω, IAS=15.5A

③脉冲测试: 脉冲宽度 $\leq 300 \mu s$, 占空比 $\leq 2 \%$



4. 特性曲线

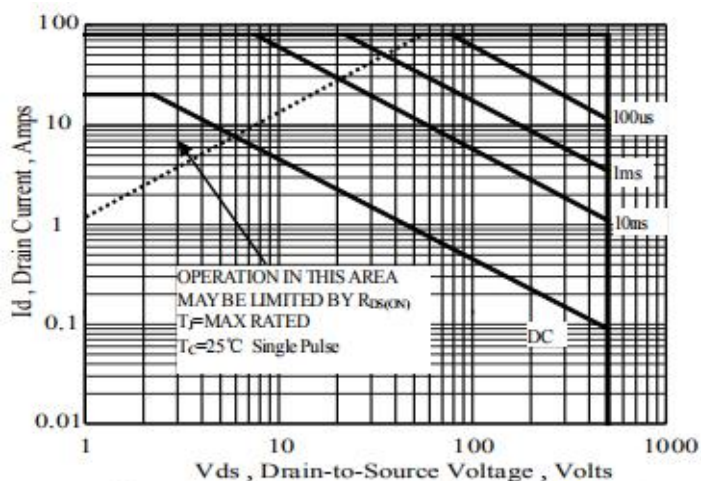


Figure 1 Maximum Forward Bias Safe Operating Area

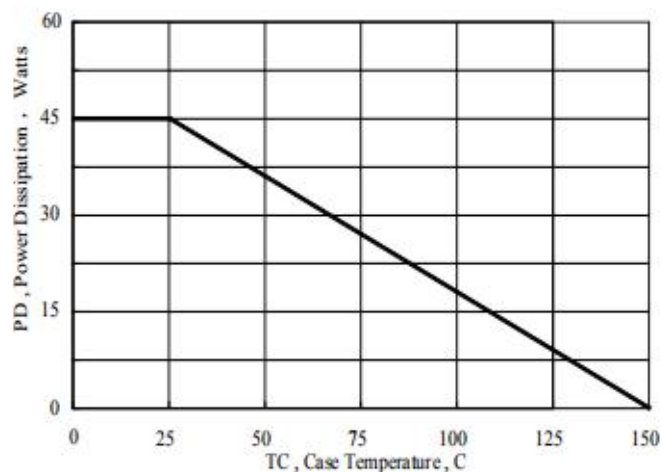


Figure 2 Maximum Power Dissipation vs Case Temperature

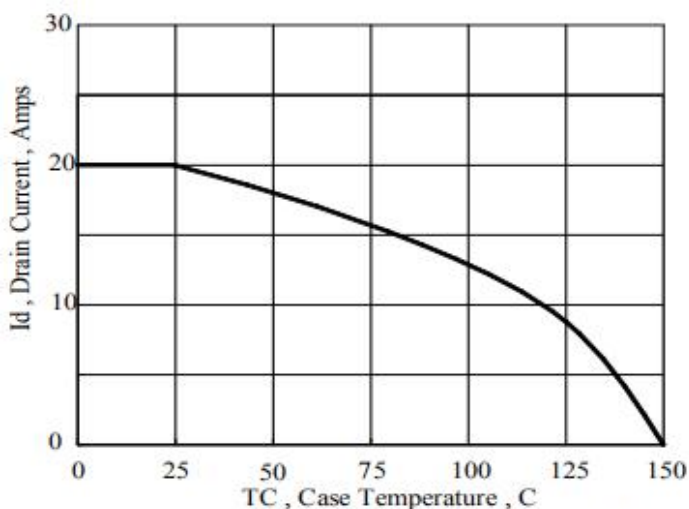


Figure 3 Maximum Continuous Drain Current vs Case Temperature

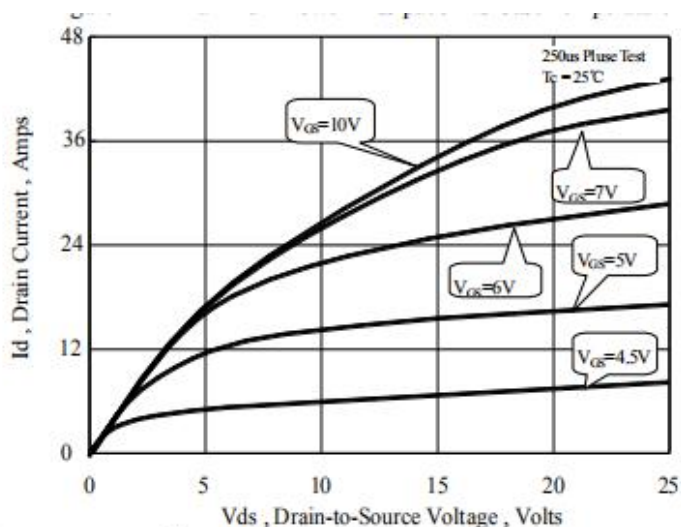
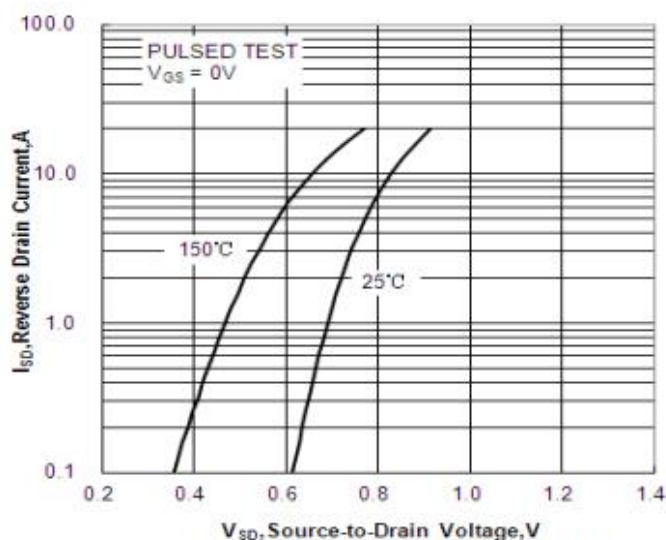
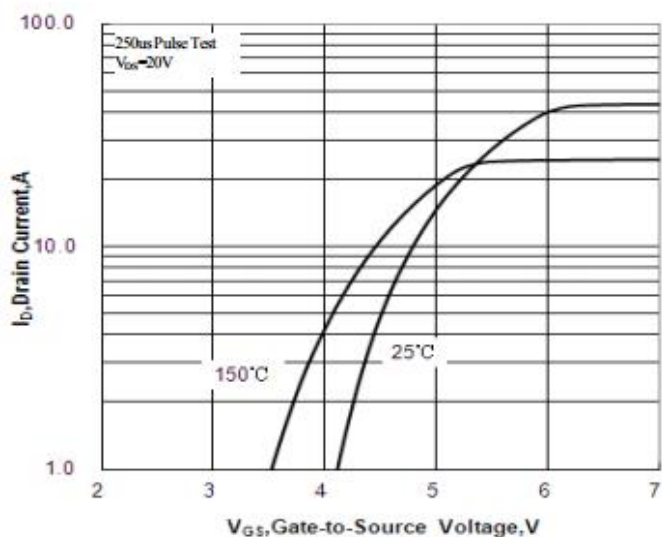
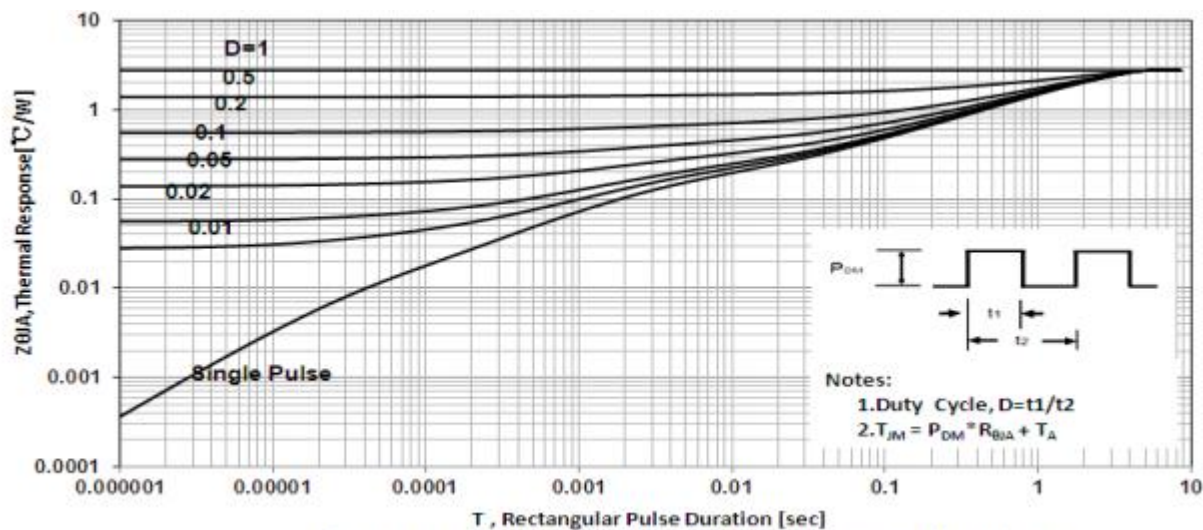


Figure 4 Typical Output Characteristics



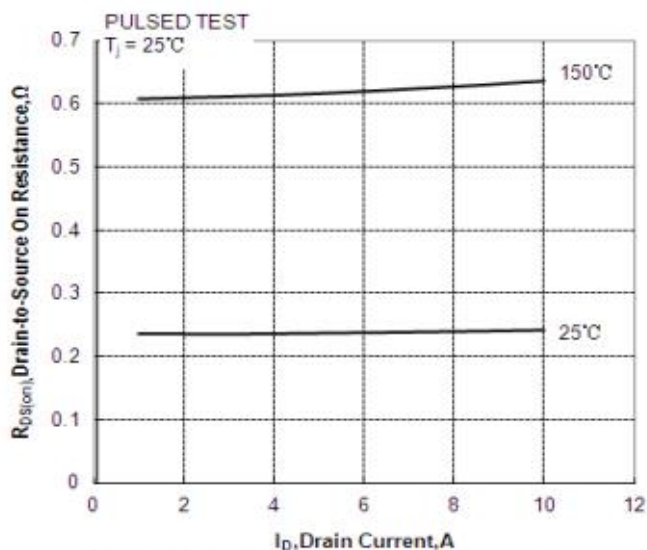


Figure 8 Typical Drain to Source ON Resistance vs Drain Current

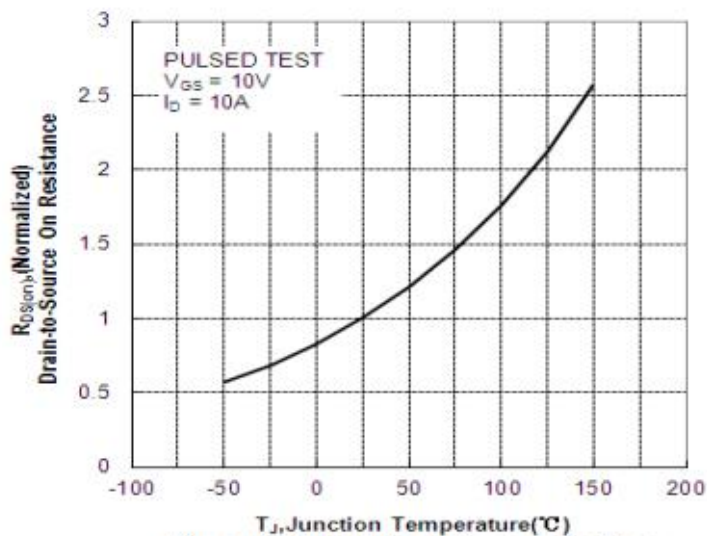


Figure 9 Typical Drain to Source ON Resistance vs Junction Temperature

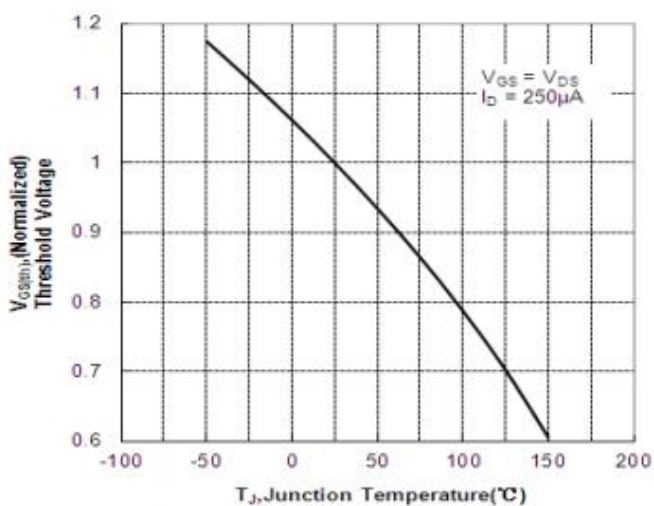


Figure 10 Typical Threshold Voltage vs Junction Temperature

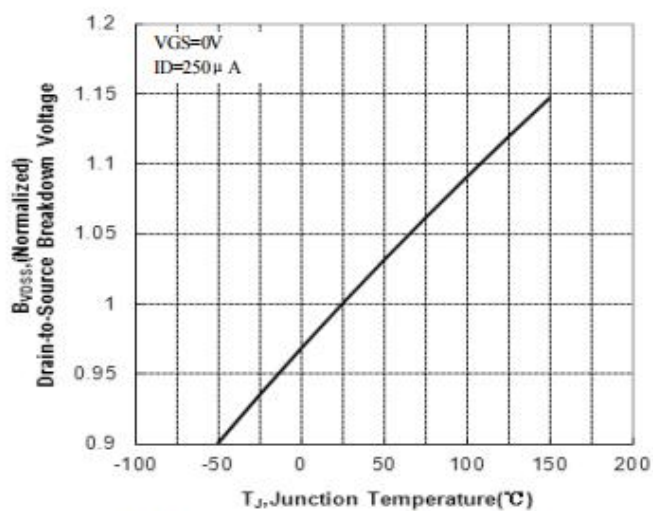


Figure 11 Typical Breakdown Voltage vs Junction Temperature

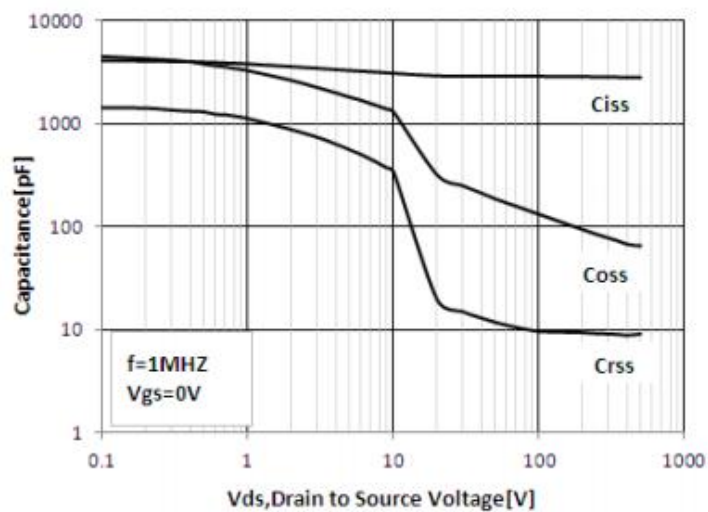


Figure 12 Typical Capacitance vs Drain to Source Voltage

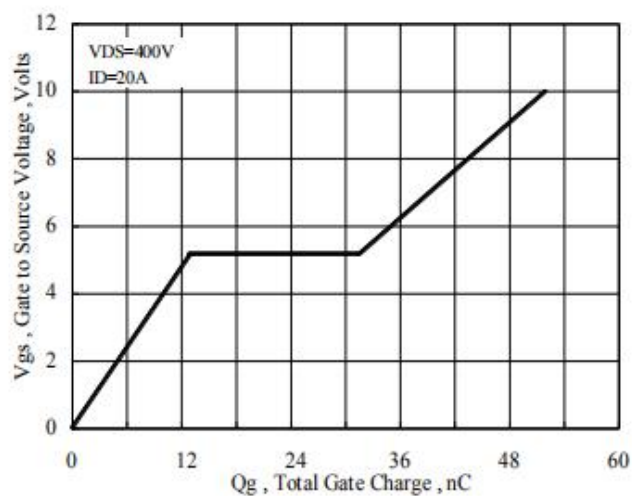


Figure 13 Typical Gate Charge vs Gate to Source Voltage