

2.3MHz, 6V/1.0A Synchronous Step-Down Converter

FEATURES

- High Efficiency: Up to 94%
- 2.3MHz Frequency Operation
- 1.0A Output Current
- Input OVP 6.1V
- No Schottky Diode Required
- 2.5V to 6V Input Voltage Range
- Output Voltage as Low as 0.6V
- 100% Duty Cycle in Dropout
- Low Quiescent Current: 40 μ A
- Slope Compensated Current Mode Control for Excellent Line and Load Transient Response
- Short Circuit Protection
- Thermal Fault Protection
- Inrush Current Limit and Soft Start
- Input over voltage protection (OVP)
- <1 μ A Shutdown Current
- SOT23-5 Package

GENERAL DESCRIPTION

The TMI3101 is a constant frequency, current mode PWM step-down converter. The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. It is ideal for powering portable equipment that runs from a single cell Lithium-Ion (Li+) battery. The output voltage can be regulated as low as 0.6V. The TMI3101 can also run at 100% duty cycle for low dropout operation, extending battery life in portable system. This device offers two operation modes, PWM control and PFM Mode switching control, which allows a high efficiency over the wider range of the load.

APPLICATIONS

- Cellular and Smart Phones
- Wireless and DSL Modems
- PDA/MID/PAD
- Digital Still and Video Cameras

APPLICATIONS

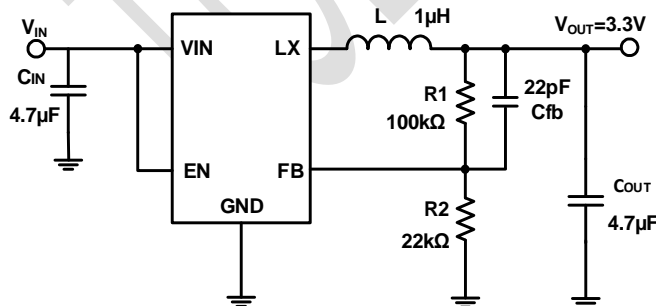
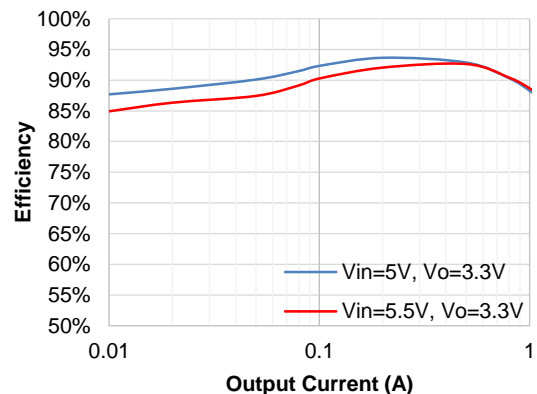


Figure 1. Basic Application Circuits

Efficiency

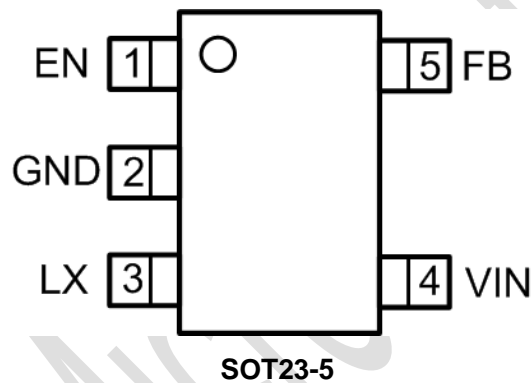
$V_{OUT}=3.3V, T_A=25^{\circ}C$



ABSOLUTE MAXIMUM RATINGS (Note 1)

Parameter	Value	Unit
Input Supply Voltage,	-0.3~7	V
LX Voltages	-0.3~ $V_{IN}+0.3$	V
EN, FB Voltage	$V_{IN}+0.6$	V
LX Voltage (<10ns transient)	-2.5~7	V
LX Voltage (<5ns transient)	-3.5~7.5	V
Junction Temperature (Note2)	-40~160	°C
Storage Temperature Range	-65~150	°C
Power Dissipation	600	mW
Lead Temperature (Soldering, 10s)	260	°C

PACKAGE/ORDER INFORMATION



Top Mark: S11XXX (S11: Device Code, XXX: Inside Code)

Part Number	Package	Top Mark	Quantity/ Reel
TMI3101	SOT23-5	S11XXX	3000

TMI3101 devices are Pb-free and RoHS compliant.

PIN DESCRIPTIONS

Pin	Name	Function
1	EN	Chip Enable Pin. Drive EN above 1.5V to turn on the part. Drive EN below 0.3V to turn it off. Do not leave EN floating.
2	GND	Ground Pin
3	LX	Power Switch Output. It is the switch node connection to Inductor.
4	VIN	Power Supply Input. Must be closely decoupled to GND with a 4.7 μ F or greater ceramic capacitor.
5	FB	Output Voltage Feedback Pin. An internal resistive divider divides the output voltage down for comparison to the internal reference voltage.

ESD RATING

Items	Description	Value	Unit
V _{ESD}	Human Body Model for all pins	± 2000	V

JEDEC specification JS-001

RECOMMENDED OPERATING CONDITIONS

Items	Description	Min	Max	Unit
Voltage Range	VIN	2.5	6.0	V
T _J	Operating Junction Temperature Range	-40	125	$^{\circ}$ C

THERMAL RESISTANCE (Note 3)

Items	Description	Value	Unit
θ_{JA}	Junction-to-ambient thermal resistance	220	$^{\circ}$ C/W
θ_{JB}	Junction-to-board thermal resistance	36	$^{\circ}$ C/W
θ_{JC}	Junction-to-case thermal resistance	62	$^{\circ}$ C/W

ELECTRICAL CHARACTERISTICS

(V_{IN}=V_{EN}=3.6V, V_{OUT}=1.8V, T_A = 25°C, unless otherwise noted.)

Parameter	Conditions	Min	Typ	Max	Unit
Input Voltage Range		2.5		6.0	V
Input OVP			6.1		V
UVLO Threshold			2.3		V
Input DC Supply Current					
PWM Mode	V _{OUT} = 90%, I _{load} =0mA		140	300	μA
PFM Mode	V _{OUT} = 105%, I _{load} =0mA		20	35	μA
Shutdown Mode	V _{EN} = 0V, V _{IN} =4.2V		0.1	1.0	μA
Regulated Feedback Voltage	T _A = 25°C	0.588	0.600	0.612	V
	T _A = 0°C ≤ T _A ≤ 85°C	0.586	0.600	0.613	V
	T _A = -40°C ≤ T _A ≤ 85°C	0.585	0.600	0.615	V
Reference Voltage Line Regulation	V _{IN} =2.5V to 6.0V		0.04	0.4	%/V
Output Voltage Line Regulation	V _{IN} = 2.5V to 6.0V		0.04	0.4	%
Output Voltage Load Regulation			0.5		%
Oscillation Frequency	V _{OUT} =100%		2.3		MHz
	V _{OUT} =0V		500		kHz
On Resistance of PMOS	I _{LX} =100mA		300		mΩ
ON Resistance of NMOS	I _{LX} =-100mA		150		mΩ
Peak Current Limit	V _{IN} = 3.6V, V _{OUT} =1.8V	1.2			A
EN Input High Voltage		1.50			V
EN Input Low Voltage				0.30	V
EN Leakage Current			±0.01	±1.0	μA
LX Leakage Current	V _{EN} =0V, V _{IN} =V _{LX} =5V		±0.01	±1.0	μA
Thermal Shutdown Threshold (Note 4)			150		°C
Thermal Shutdown Hysteresis (Note 4)			25		°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: T_J = T_A + (P_D) × θ_{JA}.

Note 3: Measured on JESD51-7, 4-layer PCB.

Note 4: Guaranteed by design.

OPERATION

The TMI3101 is a high performance 1.0A 2.3MHz monolithic step-down converter. The TMI3101 requires only three external power components (C_{IN} , C_{OUT} and L). The output voltage can be programmed with external feedback to any voltage, ranging from 0.6V to the input voltage.

At dropout, the converter duty cycle increases to 100% and the output voltage tracks the input voltage minus the $R_{DS(ON)}$ drop of the high-side MOSFET.

The internal error amplifier and compensation provides excellent transient response, load, and line regulation. Soft start function prevents input inrush current and output overshoot during start up.

FUNCTIONAL BLOCK DIAGRAM

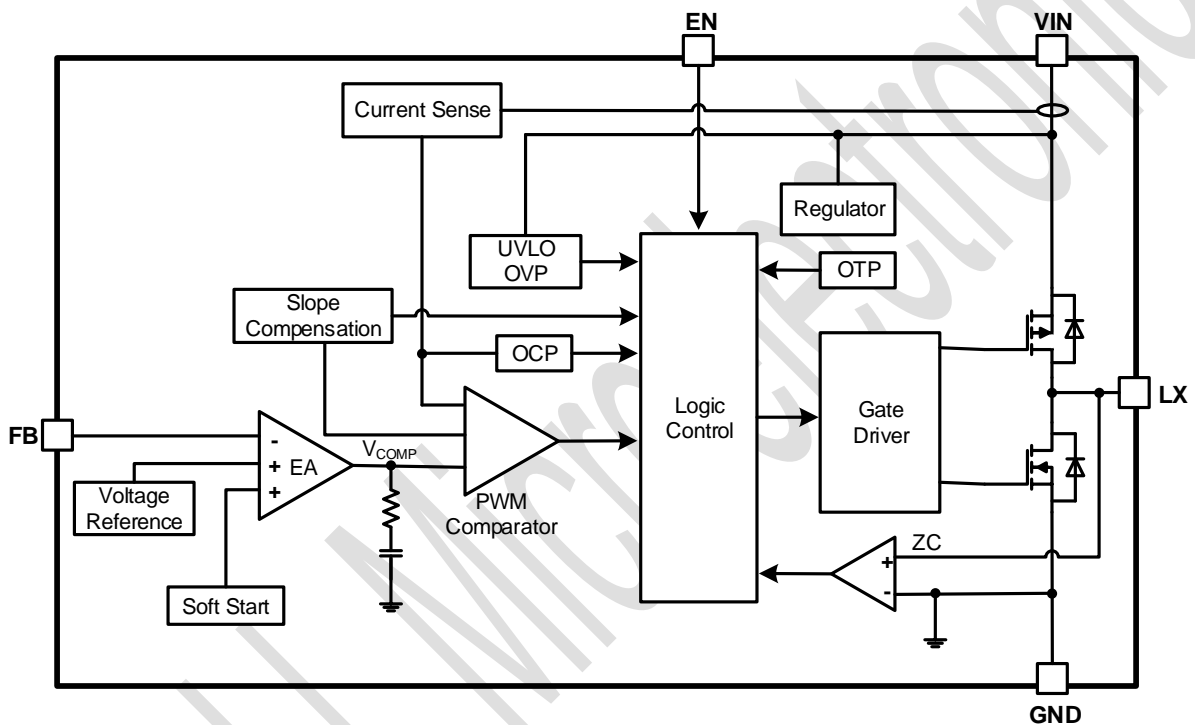
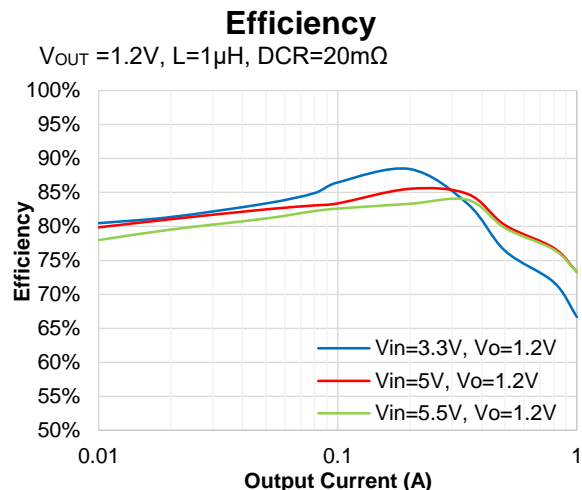
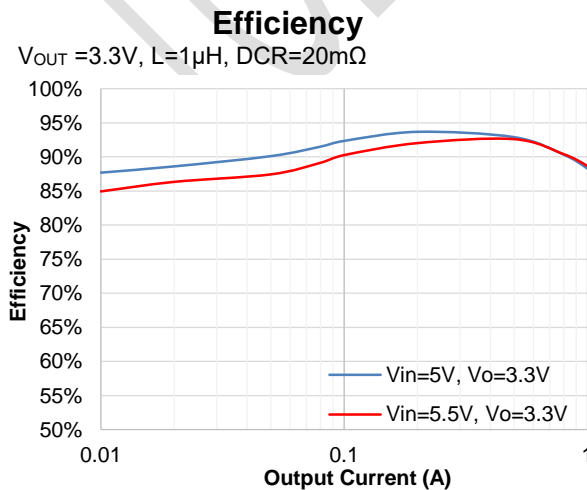


Figure 2. TMI3101 Block Diagram

FUNCTION TEST

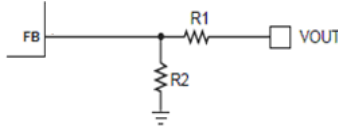


APPLICATION INFORMATION

Setting the Output Voltage

The external resistor divider is used to set the output voltage (see Typical Application on page 1). The feedback resistor R1 also sets the feedback loop bandwidth with the internal compensation capacitor. Choose R1 to be around 100kΩ for optimal transient response. R2 is then given by:

$$R_2 = \frac{R_1}{V_{out}/V_{FB} - 1}$$



Inductor Selection

For most designs, 1μH inductance can satisfy most application conditions. Inductance value is related to inductor ripple current value, input voltage, output voltage setting and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductor ripple current. Large value inductors result in lower ripple current and small value inductors result in high ripple current, so inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor.

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency should be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 4.7μF ceramic capacitor for most applications is sufficient. A large value may be used for improved input voltage filtering.

Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The output ripple V_{OUT} is determined by:

$$\Delta V_{OUT} \leq \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times \left(ESR + \frac{1}{8 \times f_{osc} \times C3} \right)$$

An effective 4.7μF ceramic can satisfy most applications.

Layout Consideration

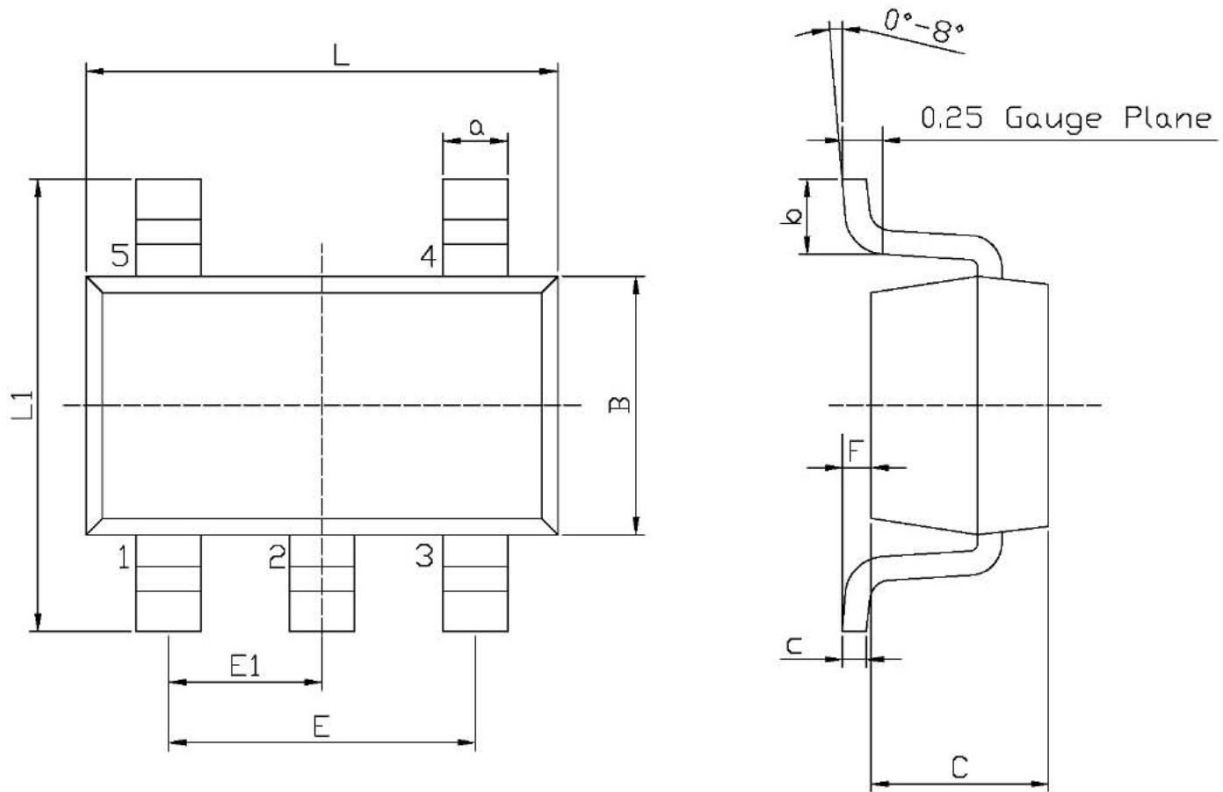
When laying out the printed circuit board, the following checking should be used to ensure proper operation of the TMI3101. Check the following in your layout:

1. The power traces, consisting of the GND trace, the LX trace and the V_{IN} trace should be kept short, direct and wide.
2. Does the (+) plates of C_{IN} connect to V_{IN} as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
3. Keep the switching node, LX, away from the sensitive V_{OUT} node.
4. Keep the (-) plates of C_{IN} and C_{OUT} as close as possible.

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PACKAGE INFORMATION

SOT23-5



Unit: mm

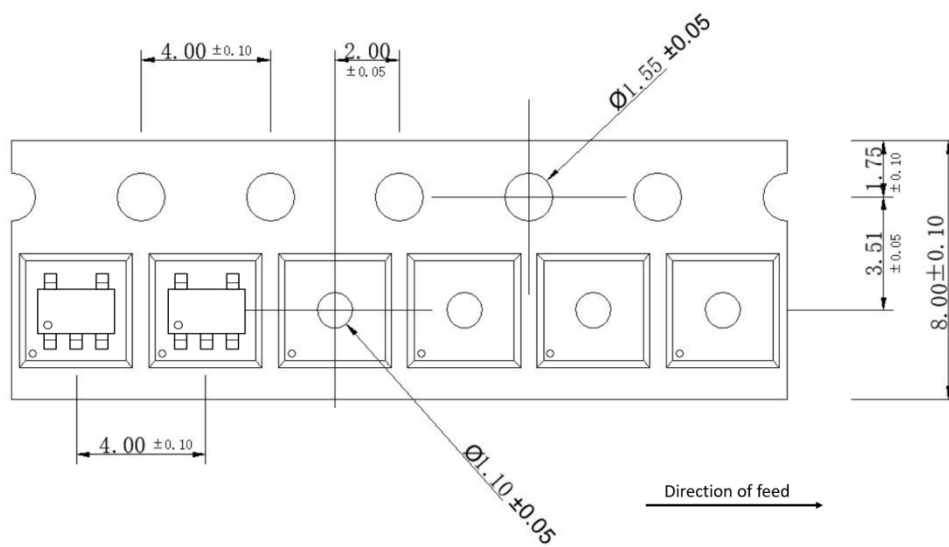
Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
L	2.82	3.02	E1	0.85	1.05
B	1.50	1.70	a	0.35	0.50
C	0.90	1.30	c	0.10	0.20
L1	2.60	3.00	b	0.35	0.55
E	1.80	2.00	F	0	0.15

Note:

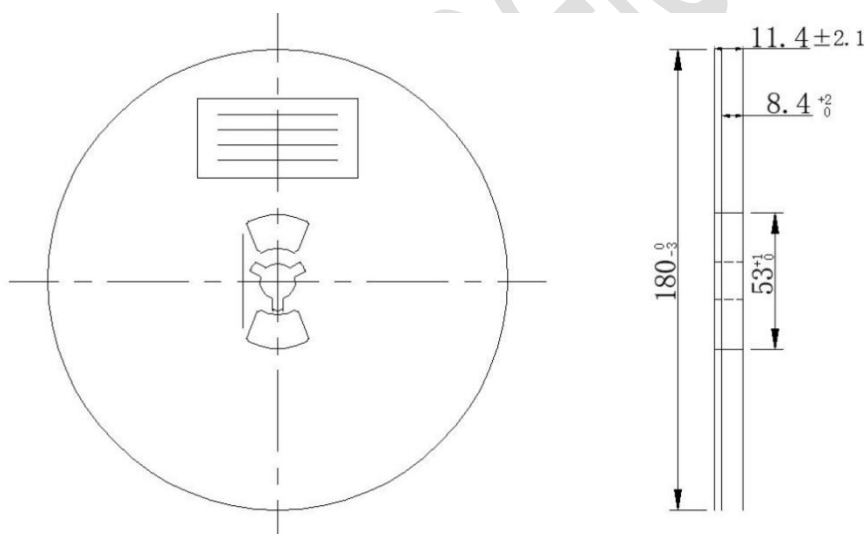
- 1) All dimensions are in millimeters.
- 2) Package length does not include mold flash, protrusion or gate burr.
- 3) Package width does not include inter lead flash or protrusion.
- 4) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 5) Pin 1 is lower left pin when reading top mark from left to right.

TAPE AND REEL INFORMATION

TAPE DIMENSIONS: SOT23-5



REEL DIMENSIONS: SOT23-5



Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 3000
- 3) MSL level is level 3.

Important Notification

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