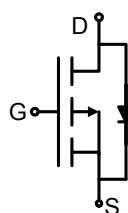
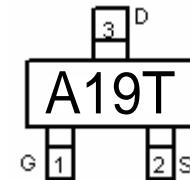


## MOSFET

<p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS} = -30V, I_D = -4.2A</math></li> <li>● <math>R_{DS(ON)} &lt; 90m\Omega @ V_{GS}=-2.5V</math></li> <li>● <math>R_{DS(ON)} &lt; 75m\Omega @ V_{GS}=-4.5V</math></li> <li>● <math>R_{DS(ON)} &lt; 55m\Omega @ V_{GS}=-10V</math></li> <li>● High power and current handling capability</li> <li>● Lead free product is acquired</li> <li>● Surface mount package</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● PWM applications</li> <li>● Load switch</li> <li>● Power management</li> </ul>	 <p><b>Schematic diagram</b></p>  <p><b>Marking and Pin Assignment</b></p>  <p><b>SOT-23 top view</b></p>
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**Absolute Maximum Ratings (TA=25°C unless otherwise noted)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	-4.2	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-30	A
Maximum Power Dissipation	$P_D$	1.2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

**Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	104	°C/W
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**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-24V, V_{GS}=0V$	-	-	-1	$\mu A$

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250µA	-0.7	-1	-1.3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A	-	48	55	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	56	75	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A		72	90	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-4.2A	-	10	-	S
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, F=1.0MHz	-	880	-	PF
Output Capacitance	C <sub>oss</sub>		-	105	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	65	-	PF
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-4.2A V <sub>GS</sub> =-10V, R <sub>GEN</sub> =6Ω	-	7	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.2A, V <sub>GS</sub> =-4.5V	-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.8	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.7	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-4.2A	-	-	-1.2	V

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

## Typical Electrical and Thermal Characteristics

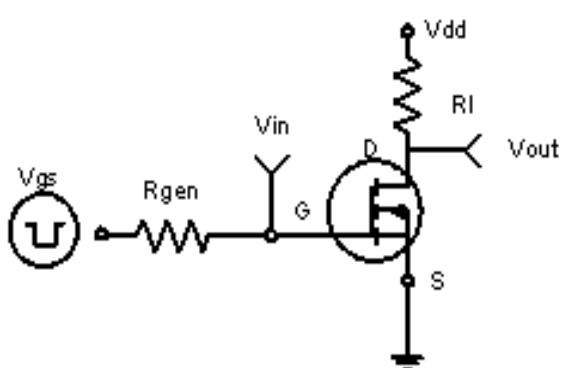


Figure 1:Switching Test Circuit

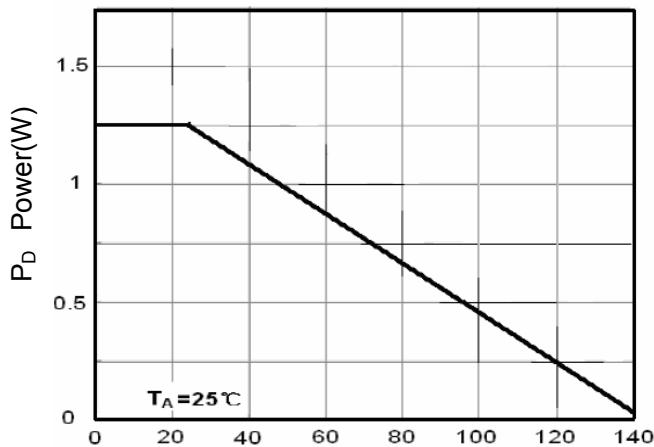
 $T_j$ -Junction Temperature( $^\circ\text{C}$ )

Figure 3 Power Dissipation

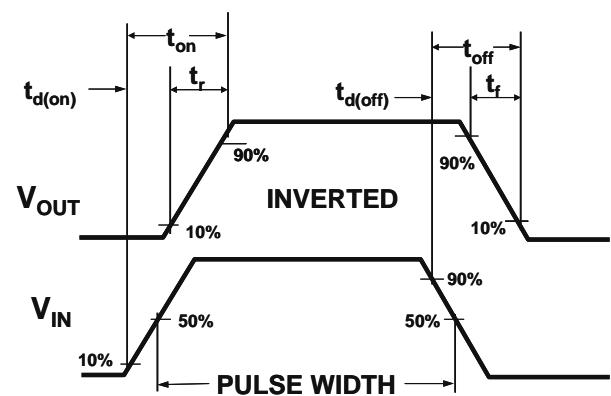


Figure 2:Switching Waveforms

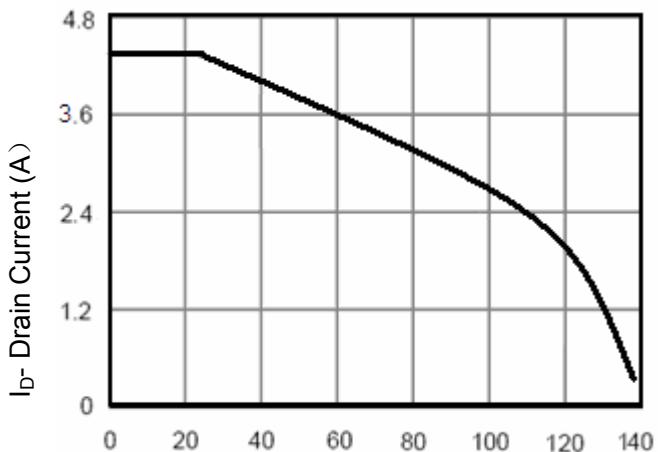
 $T_j$ -Junction Temperature( $^\circ\text{C}$ )

Figure 4 Drain Current

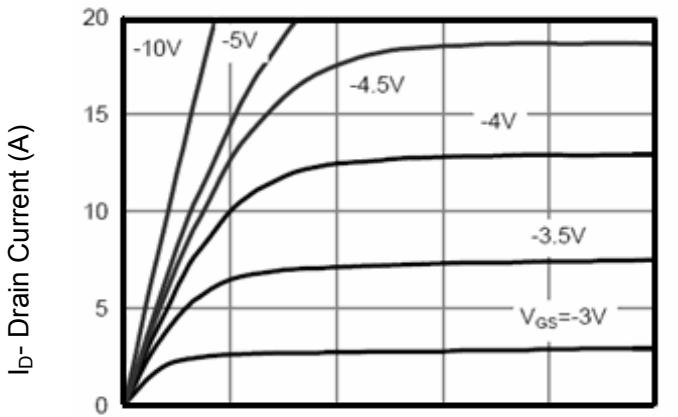
 $V_{ds}$  Drain-Source Voltage (V)

Figure 5 Output Characteristics

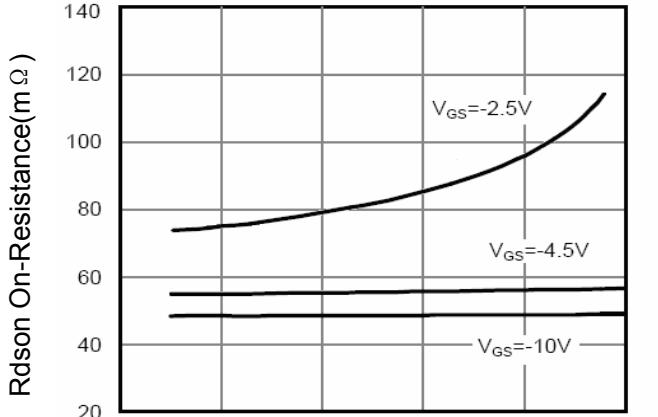
 $I_D$ - Drain Current (A)

Figure 6 Drain-Source On-Resistance

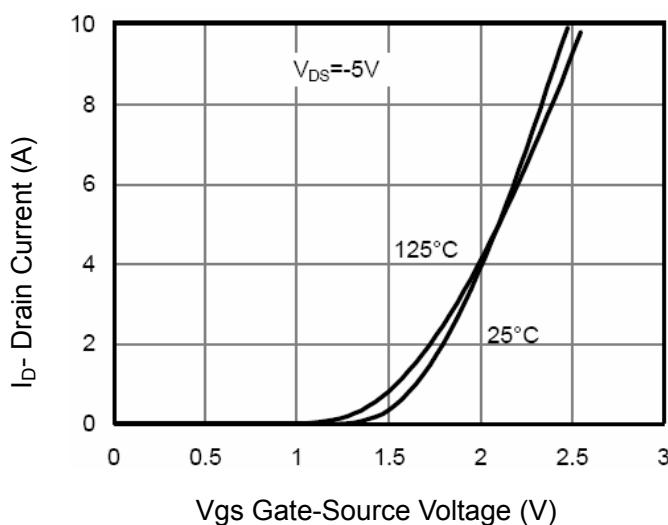


Figure 7 Transfer Characteristics

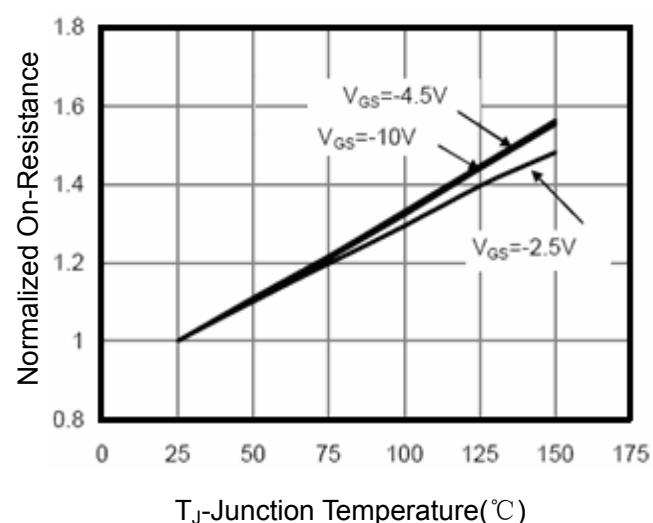


Figure 8 Drain-Source On-Resistance

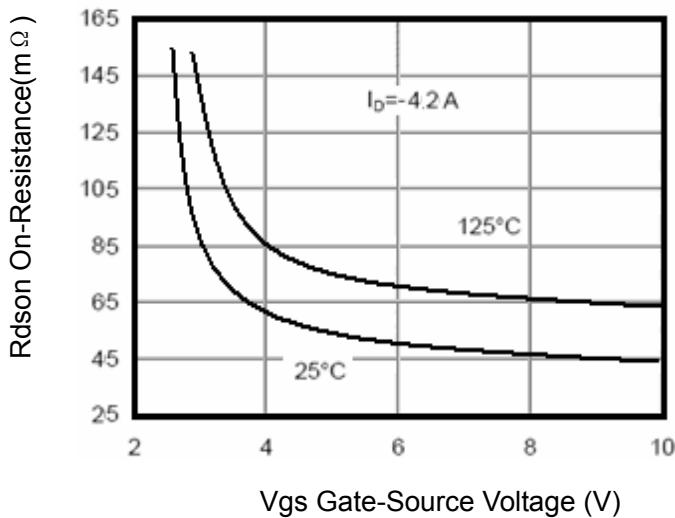


Figure 9 Rdson vs Vgs

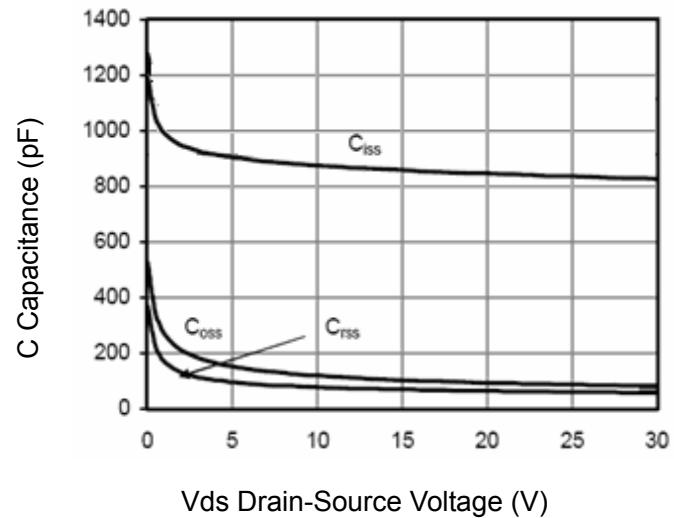


Figure 10 Capacitance vs Vds

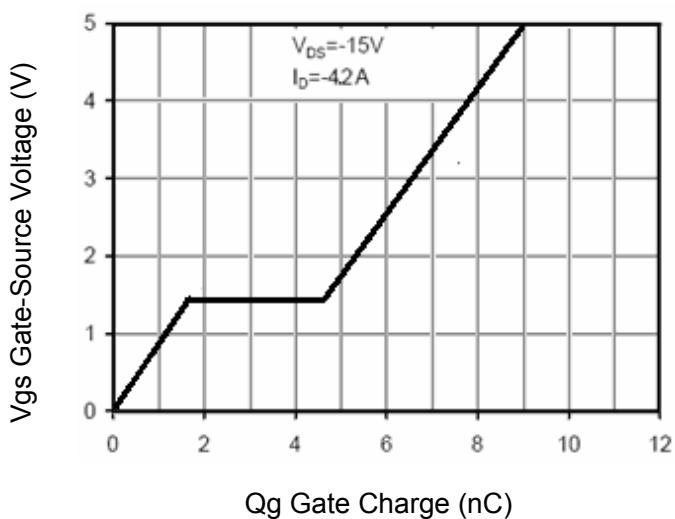


Figure 11 Gate Charge

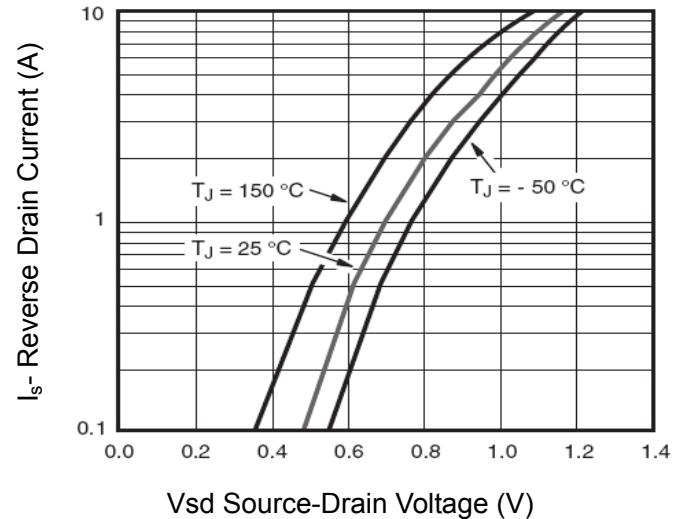


Figure 12 Source-Drain Diode Forward