

## YGMOS Technology Crop.

60V NChannel EnhancementMode MOSFET      60v 双N沟道增强型MOS管

**VDS= 60V**

**RDS(ON), Vgs@10V, Ids@5.3A = 41mΩ**

**RDS(ON), Vgs@4.5V, Ids@4.7A = 52mΩ**

### Features      特性

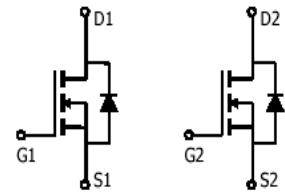
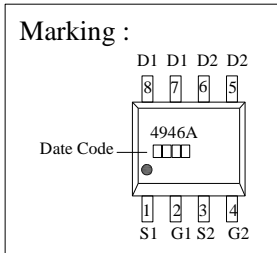
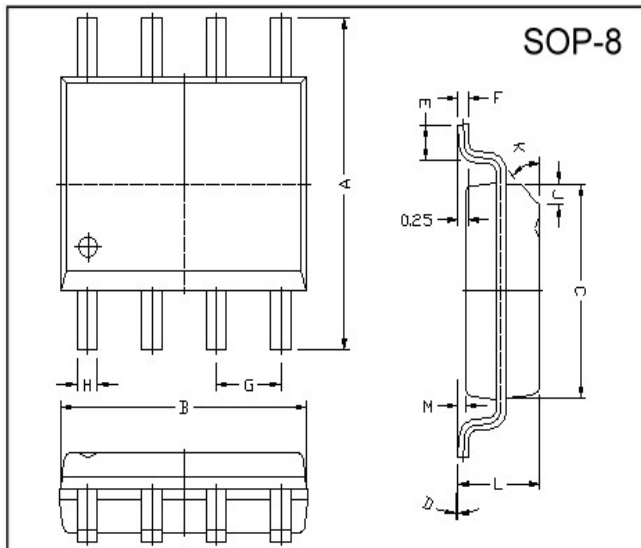
Advanced trench process technology      高级的加工技术

High Density Cell Design For Ultra Low OnResistance

极低的导通电阻高密度的单元设计

Improved ShootThrough FOM 改进的成型工艺

Package Dimensions      封装尺寸及外形图



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.80	6.20	M	0.10	0.25
B	4.80	5.00	H	0.35	0.49
C	3.80	4.00	L	1.35	1.75
D	0	8	J	0.375 REF.	
E	0.40	0.90	K	45	
F	0.19	0.25	G	1.27 TYP.	

### Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)      25°C 极限参数和热特性

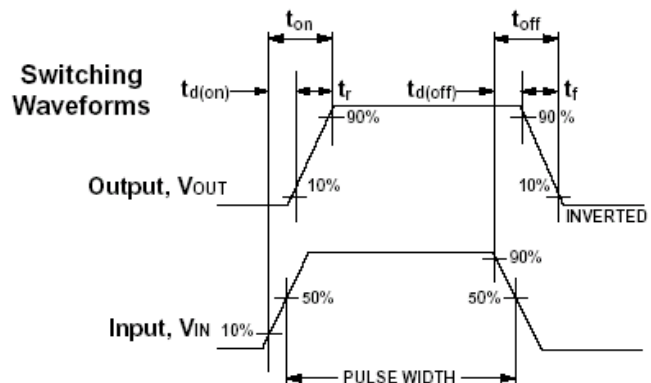
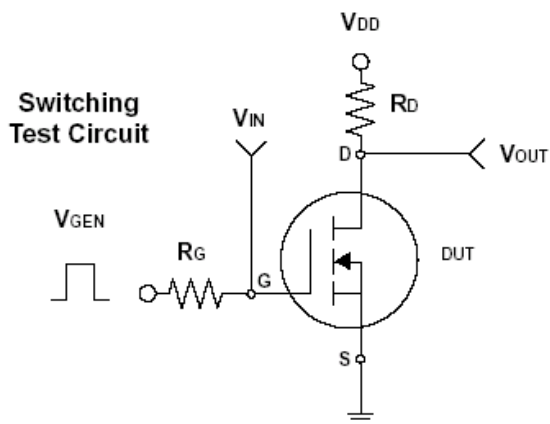
Parameter 极限参数	Symbol 符号	Limit 范围	Unit 单位	
DrainSource Voltage 漏源电压	V <sub>DS</sub>	60	V	
GateSource Voltage 栅源电压	V <sub>GS</sub>	± 20		
Continuous Drain Current 连续漏极电流	I <sub>D</sub>	6.5	A	
Pulsed Drain Current 脉冲漏极电流	I <sub>DM</sub>	30		
Maximum Power Dissipation 最大耗散功率	P <sub>D</sub>	TA = 25°C	3.7	W
		TA = 75°C	2.4	
Operating Junction and Storage Temperature Range 使用及储存温度	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	
JunctiontoAmbient Thermal Resistance (PCB mounted) 结环热阻	R <sub>θJA</sub>	62.5	°C/W	

## YGMOS Technology Crop.

### ELECTRICAL CHARACTERISTICS 一般电气特性

Parameter 参数	符号	Test Condition 测试条件	最小值	典型值	最大值	单位
<b>Static 静态参数</b>						
DrainSource Breakdown Voltage 漏源击穿电压	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	60			V
DrainSource OnState Resistance 漏源导通电阻	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 4.7A$		41	52	mΩ
DrainSource OnState Resistance 漏源导通电阻	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 5.3A$		33	41	
Gate Threshold Voltage 开启电压	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	3	V
Zero Gate Voltage Drain Current 零栅压漏极电流	$I_{DSS}$	$V_{DS} = 60V, V_{GS} = 0V$			1	μA
Gate Body Leakage 漏极短路时截止栅电流	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Forward Transconductance 正向跨导	$g_{fs}$	$V_{DS} = 15V, I_D = 5.3A$		10	—	S
<b>Dynamic 动态参数</b>						
Total Gate Charge 栅极总电荷	$Q_g$	$V_{DS} = 30V, I_D = 5.3A$ $V_{GS} = 10V$		22		nC
GateSource Charge 栅源极电荷	$Q_{gs}$			7.1		
GateDrain Charge 栅漏极电荷	$Q_{gd}$			7.5		
TurnOn Delay Time 导通延迟时间	$t_{d(on)}$	$V_{DD} = 30V, R_L = 6.8\Omega$ $I_D = 4.4A, V_{GEN} = 10V$ $R_G = 1\Omega$		13		ns
TurnOn Rise Time 导通上升时间	$t_r$			25		
TurnOff Delay Time 关断延迟时间	$t_{d(off)}$			40		
TurnOff Fall Time 关断下降时间	$t_f$			3.5		
Input Capacitance 输入电容	$C_{iss}$	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1.0\text{ MHz}$		930		pF
Output Capacitance 输出电容	$C_{oss}$			72		
Reverse Transfer Capacitance 反向传输电容	$C_{rss}$			80		
<b>SourceDrain Diode 源漏二极管参数</b>						
Max. Diode Forward Current 最大正向电流	$I_S$				3.1	A
Diode Forward Voltage 正向电压	$V_{SD}$	$I_S = 2.0A, V_{GS} = 0V$			1.2	V

Note: Pulse test: pulse width ≤ 300us, duty cycle ≤ 2% 注意: 脉冲测试: 脉冲宽度 ≤ 300us 死区 ≤ 2%



## YGMOS Technology Crop.

### Typical Characteristics (T<sub>J</sub> = 25°C •Noted)

